

Notice of Allowability**Application No.**

10/585,151

Applicant(s)

BROX, MARTIN

Examiner

Emily Pham

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to RCE filed 01/20/2011.
2. ☒ The allowed claim(s) is/are 10, 11, 13, 14, 17-19, 21, 22, 24-26 and 28-35.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: ____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date ____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date ____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date ____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 20110119, 20110127.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other ____.

/Adolf Berhane/
Adolf Berhane
Primary Examiner
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Interview on 01/27/2011 reached agreement on Examiner's Amendment to have application in conditions to allow.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Mark Peterson on 01/27/2011.

2. The application has been amended as follows:

In the claims:

Please amend claims 11, 14, 17, 18, 19, 22, 25, 26, 28, 29, and 32 as following:

11. The voltage regulation system of claim 10, wherein the variable further voltage generated by the further device can be higher than the essentially constant voltage generated by the first device.

14. The voltage regulation system of claim 10, wherein the essentially constant voltage generated by the first device or a voltage derived from it, and the variable further voltage generated by the further device, or a voltage derived from it, can be used for controlling a voltage regulation circuit device.

17. The voltage regulation system of claim 10, wherein, in the activated state of the further device, the height of the level of a reference voltage used for a voltage regulation circuit device is determined by whichever of the essentially constant voltage generated by the first device and the variable further voltage generated by the further device exhibits the higher level.

18. The voltage regulation system of claim 10, wherein, in the deactivated state of the further device, the height of the level of a reference voltage used for a voltage regulation system circuit device is determined by the essentially constant voltage generated by the first device.

19. A method for the regulation of voltage comprising:

changing a first voltage into a second voltage, wherein the second voltage exhibits a lower voltage level than the first voltage, the first voltage having a first nominal value and the second voltage having a second nominal value;

generating an essentially constant voltage from the first voltage, or a voltage derived from it, to provide the essentially constant voltage on a first line despite fluctuations of the first voltage above the first nominal value;

generating a variable further voltage from the first voltage or a voltage derived from it to provide the variable further voltage on a second line directly connected to the first line, the variable further voltage tracking the first voltage such that in response to the first voltage rising above the first nominal value, the variable further voltage rises in proportion to the first voltage, wherein the variable further voltage can be higher than the essentially constant voltage; and

changing the essentially constant voltage to provide the second voltage having the second nominal value in a first state and changing whichever voltage is greater from among the essentially constant voltage and the variable further voltage to provide the second voltage in a second state, wherein in the second state the second voltage rises above the second nominal value in response to the first voltage rising above the first nominal value.

22. The voltage regulation system of claim 21, wherein the variable further voltage can be higher than the essentially constant voltage.

25. The voltage regulation system of claim 21, wherein the voltage generated by the first device and the variable further voltage can be used for controlling a voltage regulation circuit device.

26. The voltage regulation system of claim 21, wherein the essentially constant voltage and the variable further voltage can be used as a reference voltage for a voltage regulation circuit device.

28. The voltage regulation system of claim 21, wherein, in the activated state of the further device, the height of the level of a reference voltage used for a voltage regulation circuit device is determined by whichever voltage from among the essentially constant voltage and the variable further voltage exhibits the higher level.

29. The voltage regulation system of claim 21, wherein, in the deactivated state of the further device, the height of the level of a reference voltage used for a voltage regulation system circuit device is determined by the essentially constant voltage generated by the first device or the voltage derived from it.

32. A voltage regulation system comprising:
a first reference voltage generator configured to generate an essentially constant voltage from a first voltage despite fluctuations of the first voltage above a first nominal value;

a first buffer configured to buffer the essentially constant voltage to

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provide a first reference voltage on a first line;

a second reference voltage generator configured to generate a tracking voltage from the first voltage, the tracking voltage tracks the first voltage such that in response to the first voltage rising above the first nominal value, the tracking voltage rises in proportion to the first voltage;

a second buffer configured to buffer the tracking voltage to provide a second reference voltage on a second line directly connected to the first line;

a device for activating and deactivating the second buffer to an activated or deactivated state; and

a voltage regulator configured to provide a second voltage based on the first voltage, the first reference voltage, and the second reference voltage,

wherein with the second buffer deactivated, the second voltage is maintained at a second nominal value, and

wherein with the second buffer activated, the second voltage rises above the second nominal value in response to the first voltage rising above a first nominal value.

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Allowable Subject Matter

3. Claims 10, 11, 13, 14, 17-19, 21, 22, 24-26, and 28-35 are allowed.
4. The following is an examiner's statement of reasons for allowance:

Regarding claim 10: prior art fails to disclose a further device for generating a variable further voltage from the first voltage or a voltage derived from it to provide the variable further voltage on a second line directly connected

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to the first line, the variable further voltage tracking the first voltage such that in response to the first voltage rising above the first nominal value, the variable further voltage rises in proportion to the first voltage; and

a device for activating and/or deactivating the further device to an activated and/or deactivated state,

wherein in the deactivated state, the second voltage is maintained at the second nominal value, and wherein in the activated state, the second voltage rises above the second nominal value in response to the first voltage rising above the first nominal value.

Regarding claim 19: prior art fails to disclose generating a variable further voltage from the first voltage or a voltage derived from it to provide the variable further voltage on a second line directly connected to the first line, the variable further voltage tracking the first voltage such that in response to the first voltage rising above the first nominal value, the variable further voltage rises in proportion to the first voltage, wherein the variable further voltage can be higher than the essentially constant voltage; and

changing the essentially constant voltage to provide the second voltage having the second nominal value in a first state and changing whichever voltage is greater from among the essentially constant voltage and the variable further voltage to provide the second voltage in a second state, wherein in the second state the second voltage rises above the second nominal value in response to the first voltage rising above the first nominal value.

Regarding claim 21: prior art fails to disclose means for generating a tracking voltage from the first voltage that tracks the first voltage such that in response to the first voltage rising above the first nominal value, the tracking voltage rises in proportion to the first voltage,

a further device for generating a variable further voltage from the tracking voltage to provide the variable further voltage on a second line directly connected to the first line; and

a device for activating and/or deactivating the further device to an activated and/or deactivated state:

wherein in the deactivated state, the second voltage is maintained at the second nominal value, and

wherein in the activated state, the second voltage rises above the second nominal value in response to the first voltage rising above the first nominal value.

Regarding claim 32: prior art fails to disclose a second reference voltage generator configured to generate a tracking voltage from the first voltage, the tracking voltage tracks the first voltage such that in response to the first voltage rising above the first nominal value, the tracking voltage rises in proportion to the first voltage;

a second buffer configured to buffer the tracking voltage to provide a second reference voltage on a second line directly connected to the first line;

a device for activating and deactivating the second buffer to an activated or deactivated state; and

a voltage regulator configured to provide a second voltage based on the first

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voltage, the first reference voltage, and the second reference voltage, wherein with the second buffer deactivated, the second voltage is maintained at a second nominal value, and wherein with the second buffer activated, the second voltage rises above the second nominal value in response to the first voltage rising above a first nominal value.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Emily Pham whose telephone number is (571)270-3046. The examiner can normally be reached on Mon-Thu (7:00AM - 6:00PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Monica Lewis can be reached on (571) 272 - 1838. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Adolf Berhane/
Adolf Berhane
Primary Examiner
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February 11, 2011

/EP/